

MAXIMUM RATINGS

Rating	Symbol	2N3252	2N3253	2N3444	Unit
Collector-Emitter Voltage	V _{CEO}	30	40	50	V _{dc}
Collector-Base Voltage	V _{CBO}	60	75	80	V _{dc}
Emitter-Base Voltage	V _{EBO}	5.0			V _{dc}
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	1.0 5.71			Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	5.0 28.6			Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	- 65 to + 200			°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC} R _{θJA}	35 0.175	°C/W °C/mW

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage(1) (I _C = 10 mAdc, pulsed, I _B = 0)	V _{(BR)CEO}	30 40 50	—	V _{dc}
Collector-Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	V _{(BR)CBO}	60 75 80	—	V _{dc}
Emitter-Base Breakdown Voltage (I _E = 10 μAdc, I _C = 0)	V _{(BR)EBO}	5.0	—	V _{dc}
Collector Cutoff Current (V _{CE} = 40 Vdc, V _{EB(off)} = 4.0 Vdc) (V _{CE} = 60 Vdc, V _{EB(off)} = 4.0 Vdc)	I _{CEX}	—	0.5 0.5	μAdc
Collector Cutoff Current (V _{CB} = 40 Vdc, I _E = 0) (V _{CB} = 40 Vdc, I _E = 0, T _A = 100°C) (V _{CB} = 60 Vdc, I _E = 0) (V _{CB} = 60 Vdc, I _E = 0, T _A = 100°C)	I _{CBO}	—	0.50 75.0 0.50 75.0	μAdc
Emitter Cutoff Current (V _{BE} = 4.0 Vdc, I _C = 0)	I _{EBO}	—	0.05	μAdc
Base Cutoff Current (V _{CE} = 40 Vdc, V _{EB(off)} = 4.0 Vdc) (V _{CE} = 60 Vdc, V _{EB(off)} = 4.0 Vdc)	I _{BL}	—	0.50 0.50	μAdc
ON CHARACTERISTICS				
DC Current Gain(1) (I _C = 150 mAdc, V _{CE} = 1.0 Vdc)	h _{FE}	30 25 20	—	—
(I _C = 500 mAdc, V _{CE} = 1.0 Vdc)		30 25 20	90 75 60	
(I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)		25 20 15	— — —	

**2N3252
2N3253
2N3444**

GENERAL PURPOSE

NPN SILICON

**JAN, JTX AVAILABLE
2N3253, 2N3444
CASE 79, STYLE 1
TO-39 (TO-205AD)**

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2N3252, 2N3253, 2N3444

ELECTRICAL CHARACTERISTICS (continued) ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
Collector-Emitter Saturation Voltage(1) ($I_C = 150\text{ mAdc}, I_B = 15\text{ mAdc}$)	$V_{CE(sat)}$	—	0.3	Vdc
			0.35	
			0.5	
			0.60	
(1) ($I_C = 500\text{ mAdc}, I_B = 500\text{ mAdc}$)		—	0.5	
			0.60	
			1.0	
			1.2	
Base-Emitter Saturation Voltage(1) ($I_C = 150\text{ mAdc}, I_B = 15\text{ mAdc}$) ($I_C = 500\text{ mAdc}, I_B = 50\text{ mAdc}$) ($I_C = 1.0\text{ Adc}, I_B = 100\text{ mAdc}$)	$V_{BE(sat)}$	—	1.0	Vdc
			1.3	
			1.8	
			1.8	

SMALL-SIGNAL CHARACTERISTICS

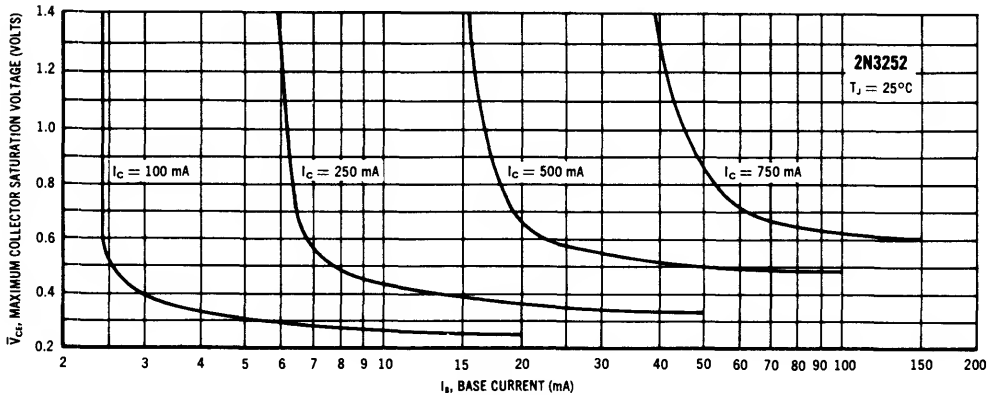
Current-Gain — Bandwidth Product ($I_C = 50\text{ mAdc}, V_{CE} = 10\text{ Vdc}, f = 100\text{ MHz}$)	2N3252 2N3253, 2N3444	f_T	200 175	— —	MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}, I_E = 0, f = 100\text{ kHz}$)		C_{obo}	—	12	pF
Input Capacitance ($V_{EB} = 0.5\text{ Vdc}, I_C = 0, f = 100\text{ kHz}$)		C_{ibo}	—	80	pF

SWITCHING CHARACTERISTICS

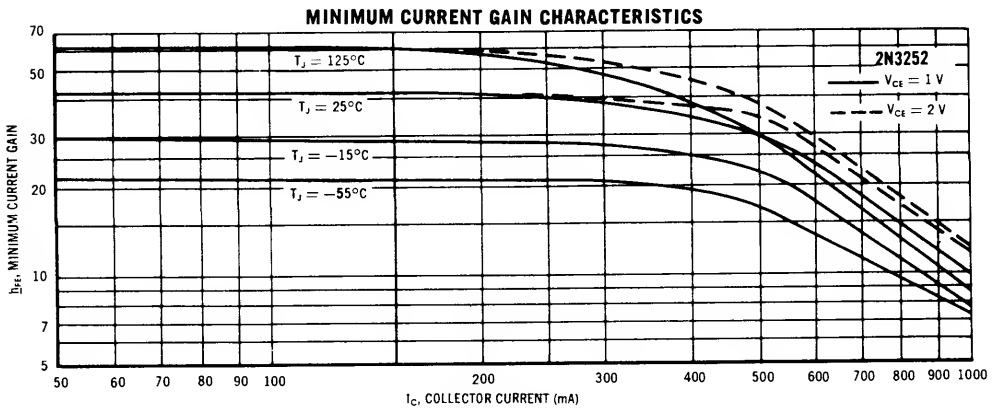
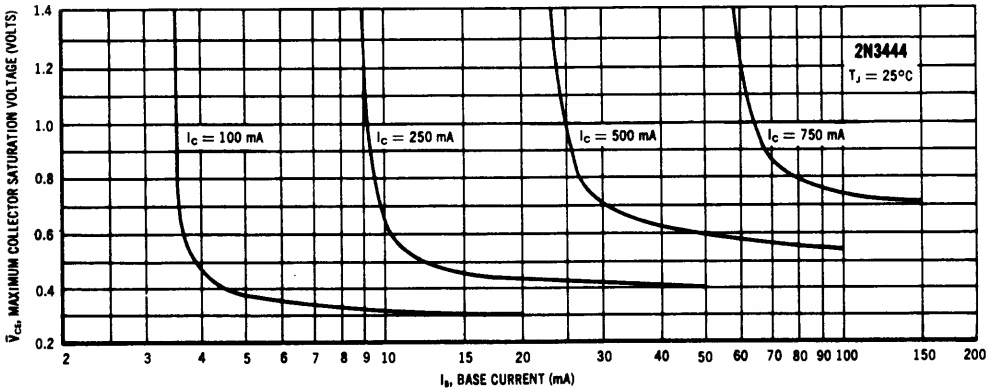
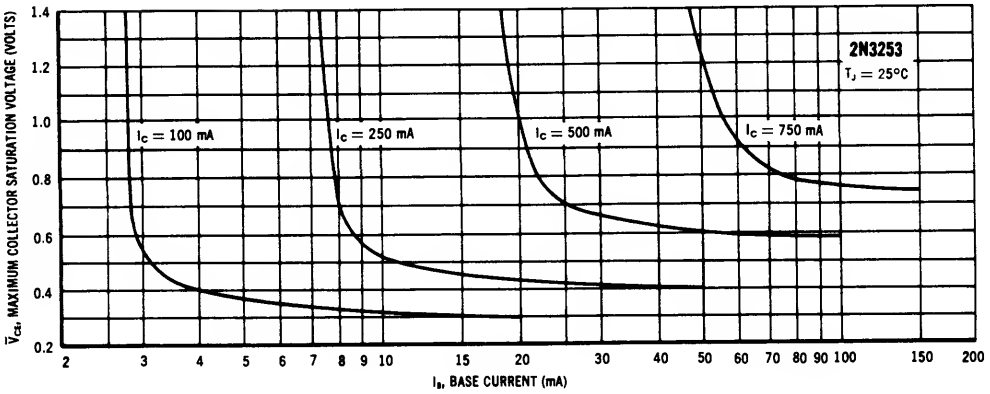
Delay Time	$I_C = 500\text{ mAdc}, I_{B1} = 50\text{ mAdc}$ $V_{CC} = 30\text{ V}, V_{BE} = 2.0\text{ V}$	2N3252 2N3253, 2N3444	t_d	—	15	ns
Rise Time			t_r	— —	30 35	ns
Storage Time	$I_C = 500\text{ mAdc}, I_{B1} = I_{B2} = 50\text{ mAdc}$ $V_{CC} = 30\text{ V}$		t_s	—	40	ns
Fall Time			t_f	—	30	ns
Total Control Charge ($I_C = 500\text{ mAdc}, I_{B1} = 50\text{ mAdc}, V_{CC} = 30\text{ V}$)			Q_T	—	5.0	nC

(1) Pulse Test: Pulse Width = 300 μs , Duty Cycle = 2.0%.

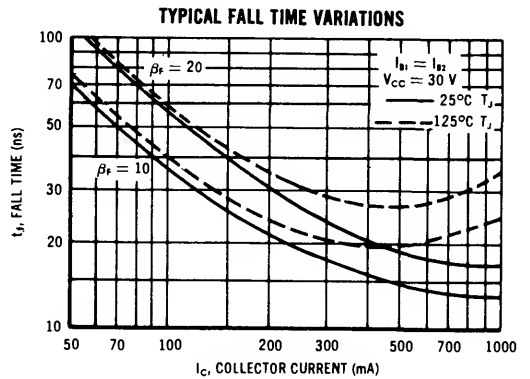
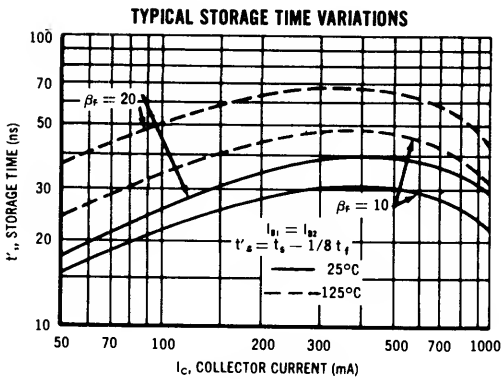
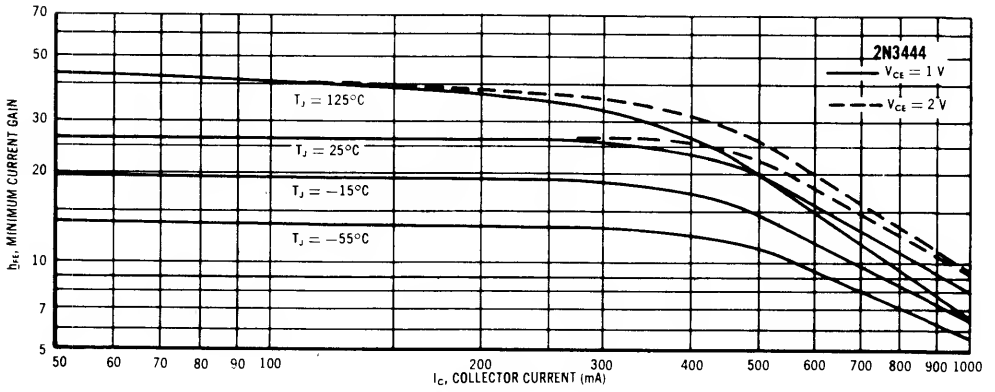
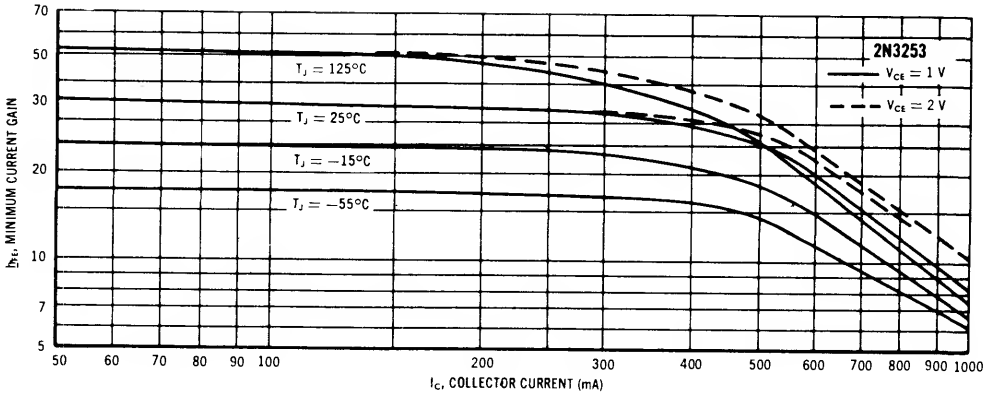
COLLECTOR SATURATION VOLTAGE CHARACTERISTICS



2N3252, 2N3253, 2N3444

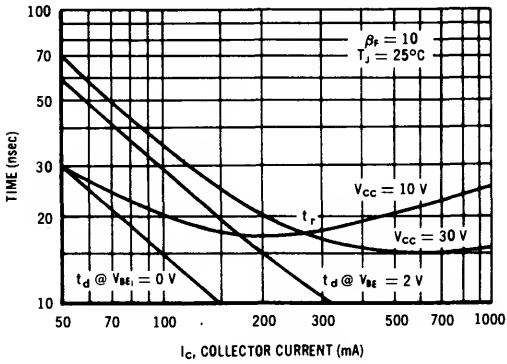


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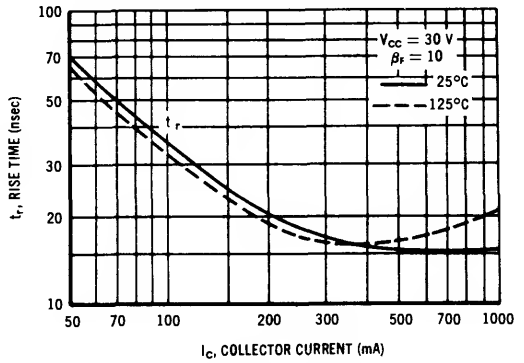


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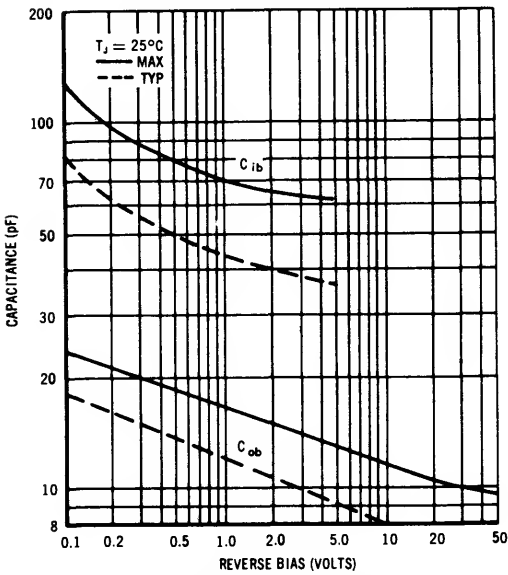
TYPICAL TURN-ON TIME VARIATIONS WITH VOLTAGE



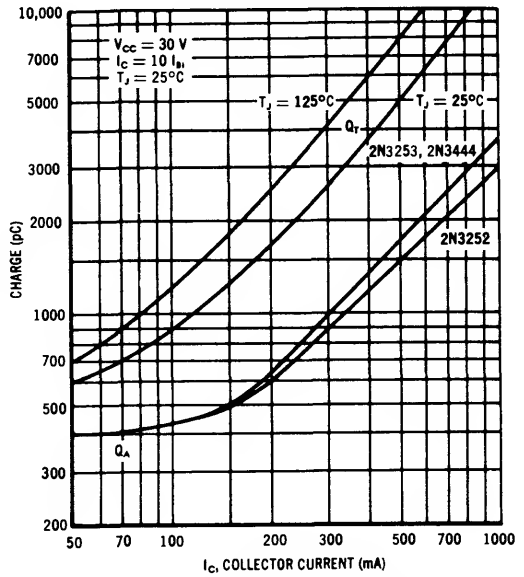
TYPICAL RISE TIME VARIATIONS WITH TEMPERATURE



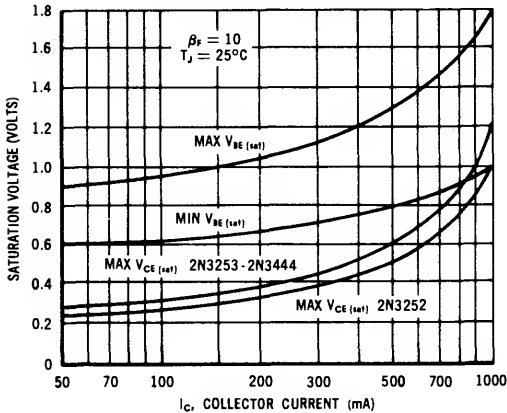
JUNCTION CAPACITANCE VARIATIONS



MAXIMUM CHARGE DATA



LIMITS OF SATURATION VOLTAGES



TYPICAL TEMPERATURE COEFFICIENTS

